



94
B1IFW

THE UNITED STATES PATENT AND TRADEMARK OFFICE

DOCKET NO.: BUR920030051US1

In re Application of:
BAJUK ET AL.

Serial No.: 10/604,071

Filed: JUNE 25, 2003

For: **CODING OF FPGA AND
STANDARD CELL LOGIC IN A
TILING STRUCTURE**

§
§
§
§
§
§
§
§
§
§

Examiner: **TUYEN P. TO**

Art Unit: **2825**

Confirmation No. 1070

LETTER TO THE OFFICIAL DRAFTSMAN

DRAWING REVIEW BRANCH
U. S. Patent and Trademark Office
Alexandria, Virginia 22313-1450

Sir:

Submitted herewith are eight (8) *replacement* drawings with corrections pursuant to the Draftsman's Drawing Review in the above-referenced allowed application.

Respectfully submitted,

Andrew J. Dillon
Registration No. 29,634
DILLON & YUDELL LLP
8911 N. Capital of Texas Hwy., Suite 2110
Austin, Texas 78759
(512) 343.6116

ATTORNEY FOR APPLICANT(S)

3/29/06
(Date)